		Av
	Application No.	Applicant(s)
Notice of Allowability	10/618,404	YOUNG, STEVEN P.
	Examiner	Art Unit
	James Cho	2819
The MAILING DATE of this communication and	noom on the cover cheet wi	th the correspondence address
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT I of the Office or upon petition by the applicant. See 37 CFR 1.31	S (OR REMAINS) CLOSED in 5) or other appropriate commined in the commined in t	n this application. If not included unication will be mailed in due course. THIS
1. This communication is responsive to the response filed 9	-30-05 .	
2. The allowed claim(s) is/are 1-7 and 21-32.		
3. Acknowledgment is made of a claim for foreign prioritý ι	under 35 U.S.C. § 119(a)-(d)	or (f).
a) All b) Some* c) None of the:		
 Certified copies of the priority documents have 	ve been received.	
2. Certified copies of the priority documents have	ve been received in Application	on No
Copies of the certified copies of the priority d	ocuments have been receive	d in this national stage application from the
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		e a reply complying with the requirements
4. A SUBSTITUTE OATH OR DECLARATION must be subtention in the substitution (PTO-152) which given the substitution (PTO-152) which given the substitution in the substitution (PTO-152) which given the substitution is substitutionally and substitution in the substitution in the substitution is substitutionally and substitution in the substitution in the substitution in the substitution is substitutionally and substitution in the substitution in		
5. 🔲 CORRECTED DRAWINGS (as "replacement sheets") mւ	ust be submitted.	
(a) I including changes required by the Notice of Draftsper	rson's Patent Drawing Review	w (PTO-948) attached
1) hereto or 2) to Paper No./Mail Date	·	
(b) including changes required by the attached Examiner Paper No./Mail Date	r's Amendment / Comment or	r in the Office action of
Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in		- ·
 DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT 		
Attachment(s) 1. Notice of References Cited (PTO-892)	5. ☐ Notice of In	formal Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	<u> </u>	ummary (PTO-413),
3. Information Disclosure Statements (PTO-1449 or PTO/SB/	Paper No.	/Mail Date Amendment/Comment
Paper No./Mail Date		
 Examiner's Comment Regarding Requirement for Deposit of Biological Material 	8. ⊠ Examiner's	Statement of Reasons for Allowance
	9.	_•

Application/Control Number: 10/618,404

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DETAILED ACTION

Receipt is acknowledged of the Amendment filed 9-30-2005.

Allowable Subject Matter

Claims 1-7 and 21-32 are allowable over the prior art of record.

The following is an examiner's statement of reasons for allowance: Although Seefeldt et al. (US PAT No. 4,978,633) teaches a hierarchical variable die size gate array architecture, one of ordinary skill in the art would not have been motivated to modify the teaching of Seefeldt et al. to further includes, among other things, the specific of the predetermined regions where each of the predetermined region in a first area and a second area comprises programmable circuits which are substantially identical and have a first function and a second function respectively (claim 1), a method of providing a plurality of tiles in a third column extending from the first side of the integrated circuit die to the second side of the integrated circuit die where the plurality of tiles in the third column having different functions than the first column (claim 21), a second column of input/output block tiles disposed on an end of the integrated circuit (claim 26), a plurality of tiles disposed in a column on an end of the integrated circuit where the plurality of tiles comprises a plurality of transceivers (claim 30), and a plurality of tiles in a second column extending from a first end to a second end where the plurality of tiles comprises memory cells (claim 32).

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably Application/Control Number: 10/618,404 Page 3

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accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to James Cho whose telephone number is 571-272-1802. The examiner can normally be reached on M-F 6:30 AM - 3:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rexford Barnie can be reached on 571-272-7492. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Yames H. Cho Primary Examiner Art Unit 2819

11-14-2005